

Electronic Acknowledgement Receipt

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Application Number:	10562869
International Application Number:	
Confirmation Number:	6418
Title of Invention:	Logic gate with a potential-free gate electrode for organic integrated circuits
First Named Inventor/Applicant Name:	Walter Fix
Customer Number:	27162
Filer:	William Squire
Filer Authorized By:	
Attorney Docket Number:	411000-144
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Reply Brief Filed	LP144replybrf.pdf	114588 6e0e63a2dae9e6e7a5c502a11ddde6b674a1fa26	no	11

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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